AMENDMENT TO THE ABSTRACT

Please substitute the following Abstract:

Attorney Docket: T2147-908626
Application No.: 10/627,977

ABSTRACT

~~Applicant: BULL S.A.~~

~~Inventors : Anne Kaszynski~~

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The present invention ~~concerns~~ relates to a method for ~~the~~ on demand functional verification of a software model ~~(40)~~ of an application specific integrated circuit ~~on demand~~ (ASIC), in a low-level programming language (for example of the HDL type), which separately handles the creation of the model and the debugging of the functional verification tests to be applied to the model of the circuit in order to create a verification platform~~, comprising the following two steps~~. The method for verification comprises a transmission mode and a verification mode. In the transmission mode ~~creation of~~ an autonomous circuit emulator (1), is created or obtained by replacing the model in a low level ~~(HDL-type)~~ programming language physically describing the circuit under design to be validated with a high level (for example C++) abstract description generating response data structures in accordance with the functional specification (20) of the design as a function of the stimuli received~~, this mode being called the "transmission mode";~~ . The verification mode includes ~~integration of the software model (40) in low level (HDL-type) language of the circuit resulting from the design into a verification platform, and creation of the connection of the previously validated autonomous circuit emulator ~~(1)~~, in parallel, to the interfaces of the software model ~~(40)~~ of the circuit, and of the connection of an environment emulator ~~(11, 21, 22); and~~. The verification ~~utilization~~ of this platform is utilized as a reference for the validation of the response data trnasmitted by the software model ~~(40)~~ of the circuit~~., this mode being called the "verification mode."~~

~~Figure 1~~

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Amendments to the Drawings:

The attached four (4) sheets of drawings include changes to Figs. 1 –4. These annotated sheets replace the original sheets of drawings filed with the application. The proposed corrections only comprise removal of the numbers "1/4" through "4/4" to conform the drawings to U.S. format, translation of the French terms in the drawings into English, and identification of blocks in the drawings to conform the drawings to the specification and claims.

Attachments: Four (4) Sheets of Annotated Marked-Up Drawings